

ETS 2015 – Program at a glance

	Monday 25.05.15			Tuesday 26.05.15				Wednesday 27.05.15				Thursday 28.05.15				Friday 29.05.15	
	Vivaldi	Verdi	Botticelli	Vivaldi	Verdi	Toscanini	Botticelli	Vivaldi	Verdi	Toscanini	Botticelli	Vivaldi	Verdi	Toscanini	Botticelli	Toscanini	
08:00																	
08:30																	
09:00				Opening				Keynote 2 H.-J. WUNDERLICH				S.7.1. Regular High Frequency and RF	S.7.2. Special Security of CPS and IoT	S.7.3. Vendor Session			
09:30				Keynote 1 D. GOODMAN				S.4.1. Regular Transistor Level Reliability		S.4.2. ETS ² Operational Test Issues		Registration (Foyer)				Keynote 1 M. AZIMANE	
10:00				Posters & Coffee (Foyer)				Coffee (Foyer)				Posters & Coffee (Foyer)				Coffee Break	
10:30																	
11:00																	
11:30				S.1.1. Regular Fault Tolerant Architectures	S.1.2. Special Biochips	S.1.3. Vendor Session		S.5.1. Regular Analog and Mixed-Signal Test	S.5.2. Panel Is Adaptive Testing the Panacea for the Future Test Problems?	S.5.3. ETS ² Differences and Bridges for System Level Test vs. Chip Level Test		S.8.1. Regular Compression	S.8.2. Panel Analog Test: Why still "à la mode" after more than 25 years of research?	S.8.3. Vendor Session		TTTC's E.J. McCluskey Doctoral Thesis Award	Session 2 Early reliability estimation role in next generation system's designs
12:00				Lunch (Venezia Restaurant)				Lunch (Venezia Restaurant)				Lunch (Venezia Restaurant)				Lunch	
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*The ETTTC Fringe Meeting will be held on Wednesday, May 27th, in the Caravaggio Room.

**The ETS'16 Executive Meeting will be held on Thursday, May 28th, in the Caravaggio Room.